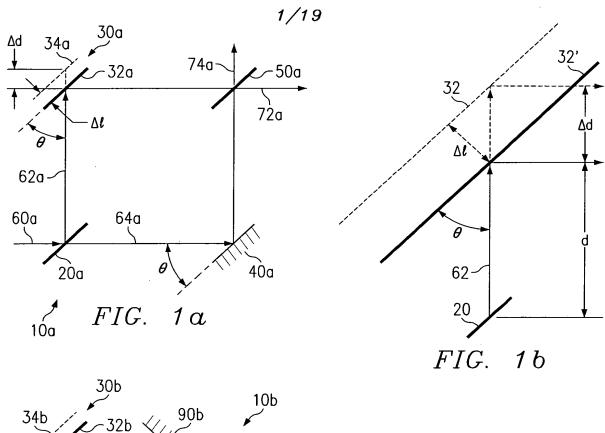
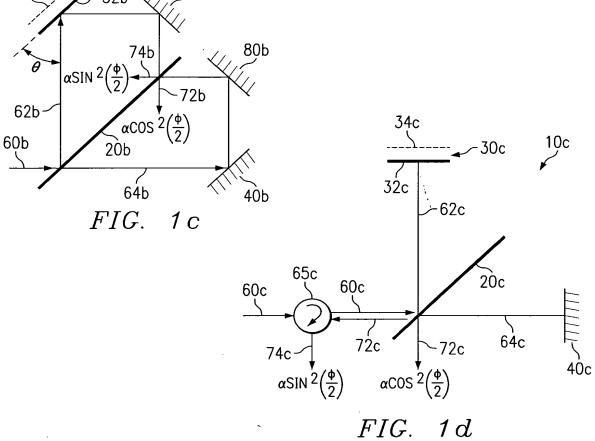
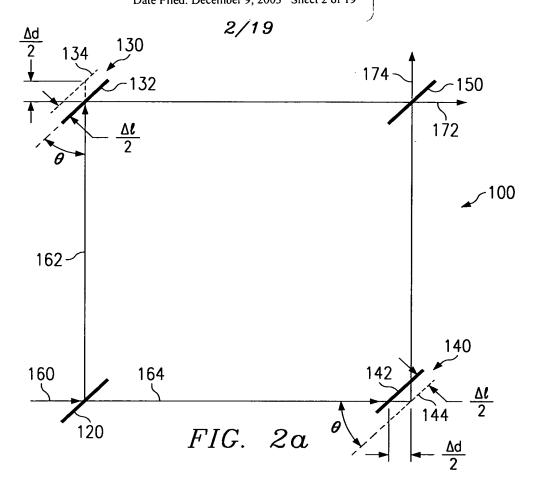
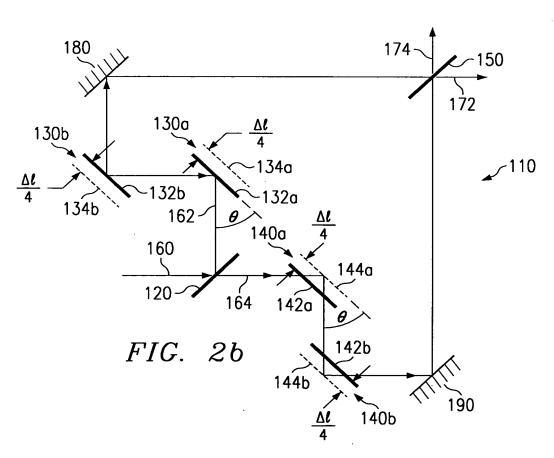
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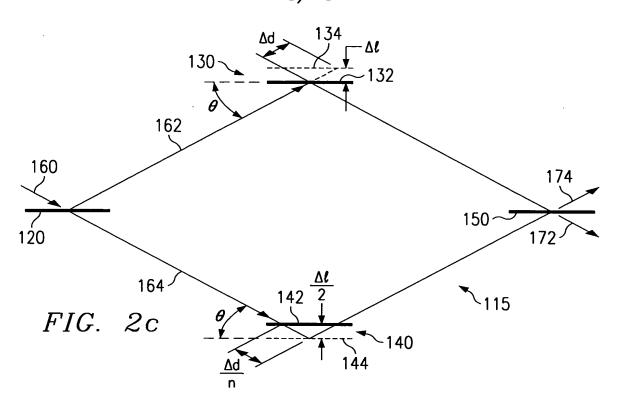
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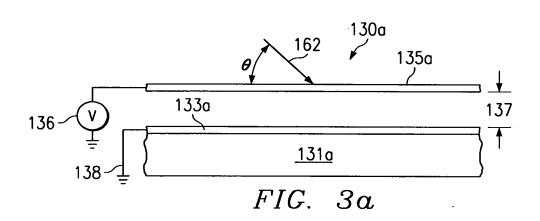


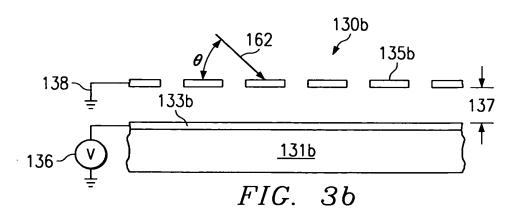


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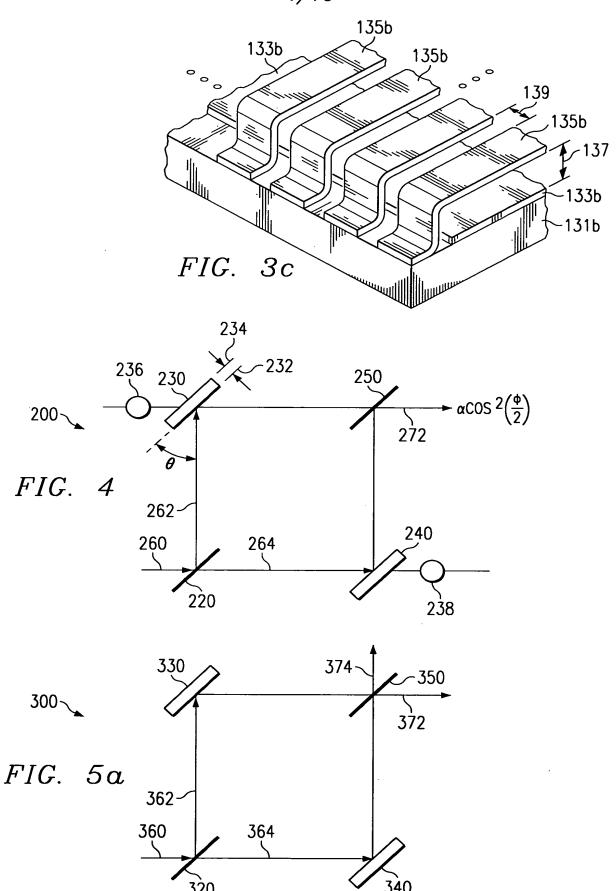


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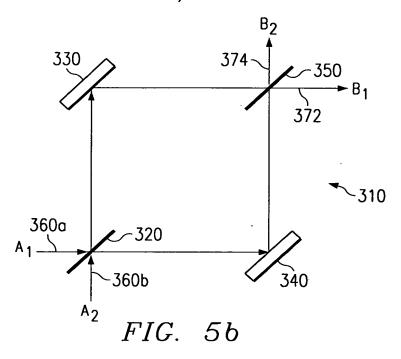
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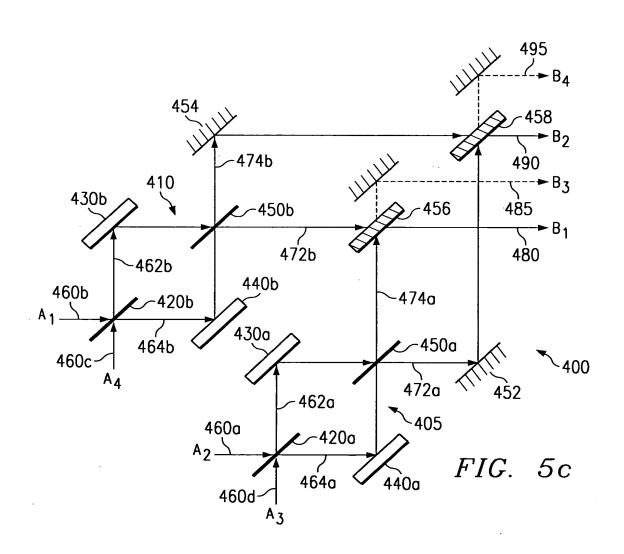
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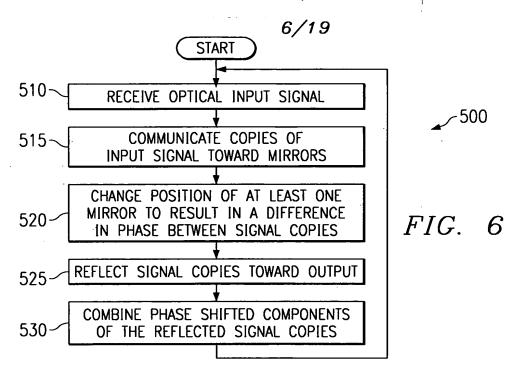
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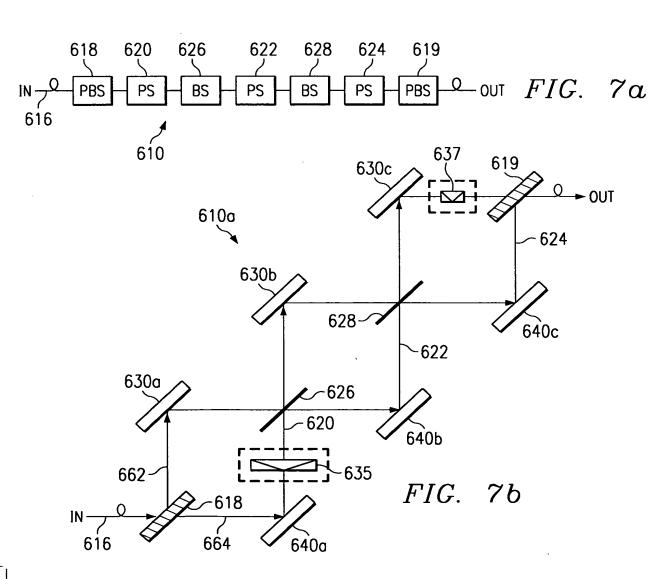




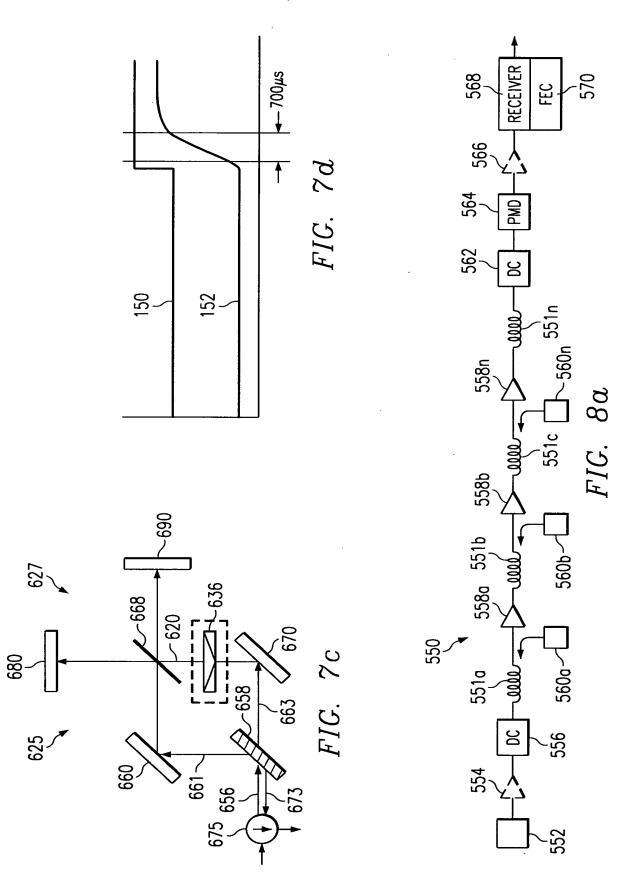
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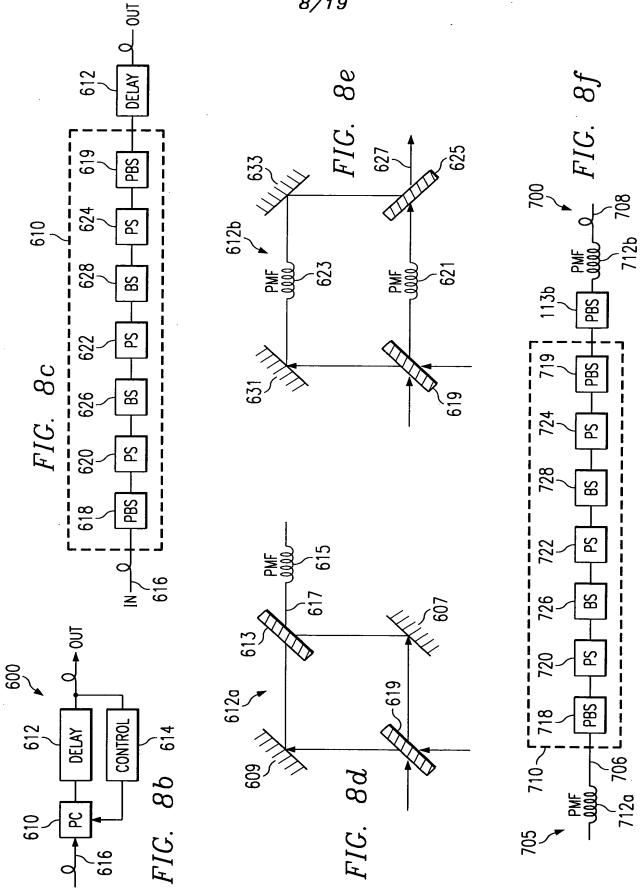
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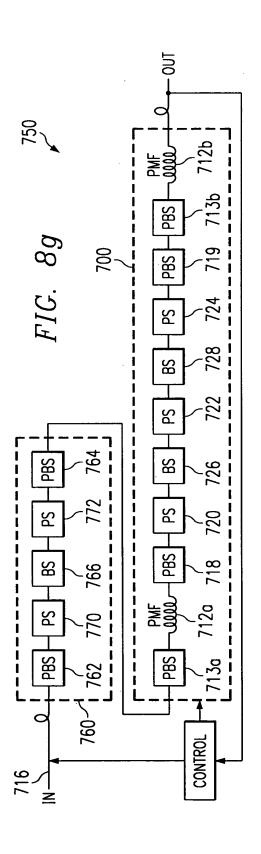




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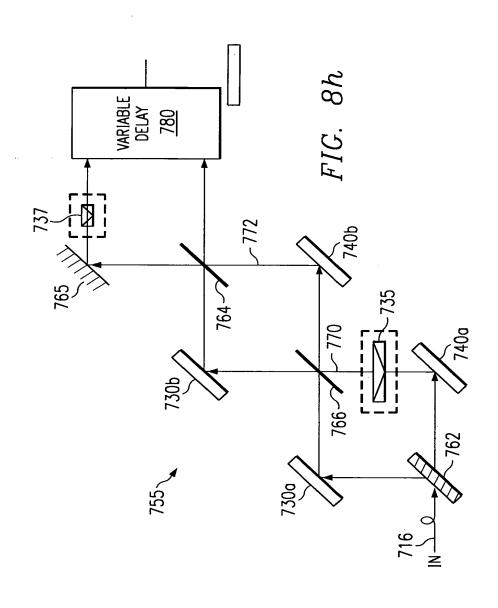
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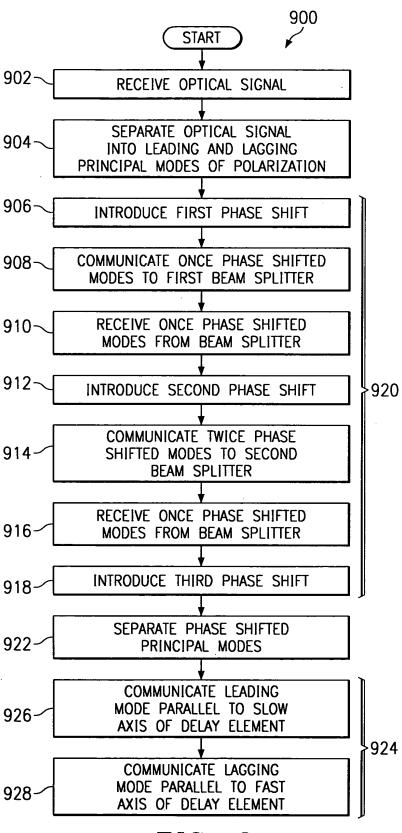


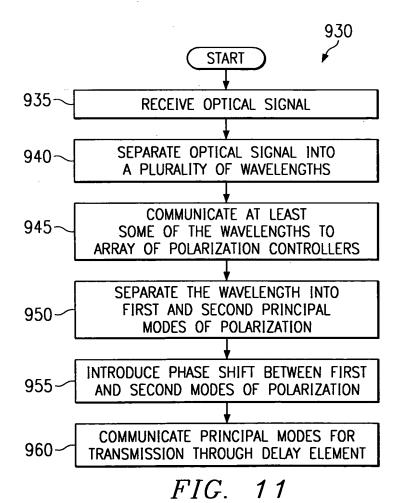
FIG. 9

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12/19 823 800 802 806n 808n 812 817 **ARRAY** O_OUT **DELAY** OF PC'S 816 <u>810</u> 807 8080 804 806a 821 **DETECTORS** -823 821n-CONTROL **ARRAY** 814

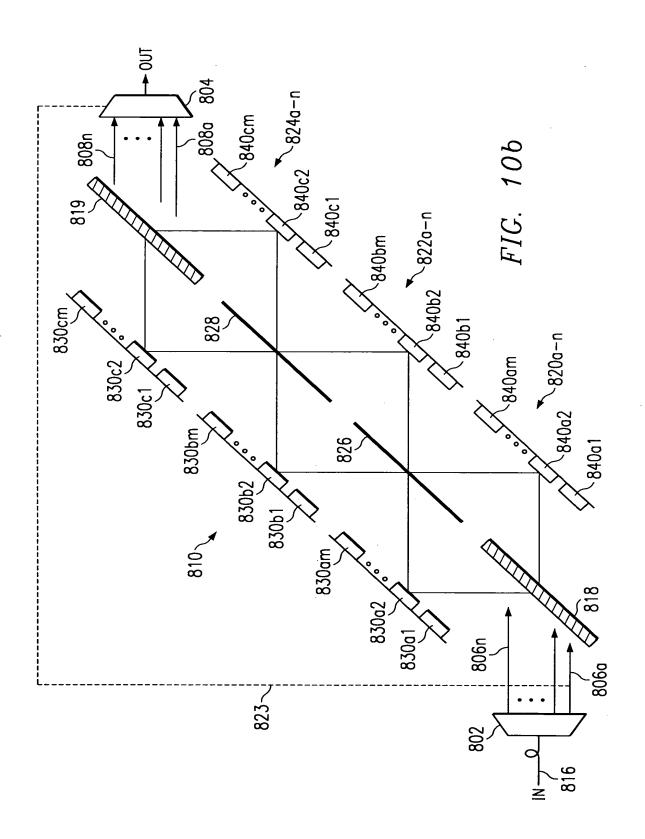
FIG. 10a



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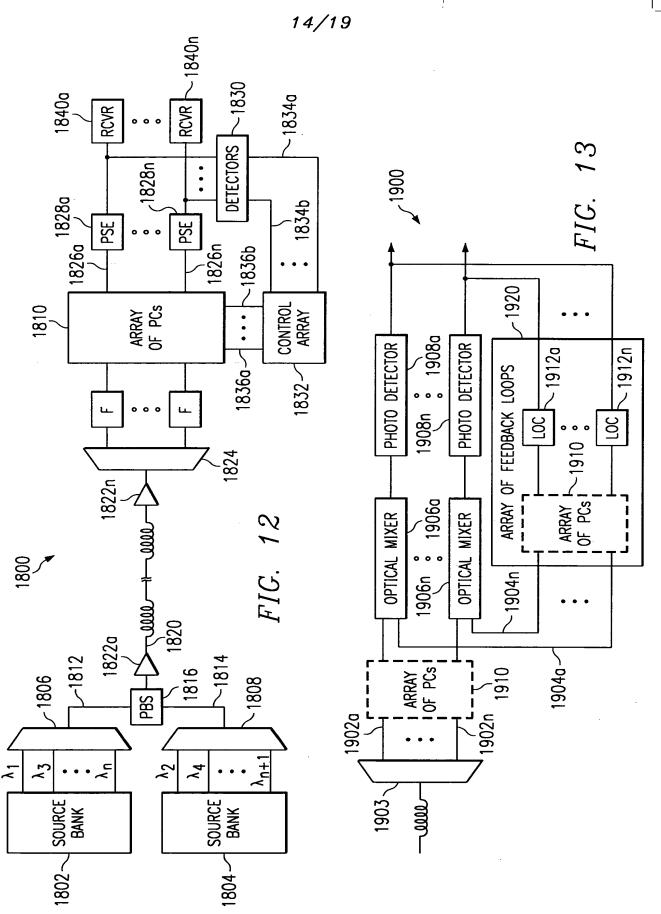
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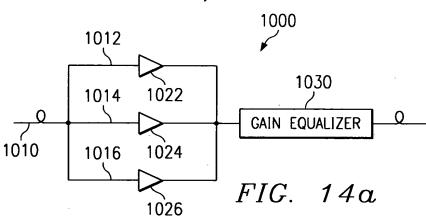
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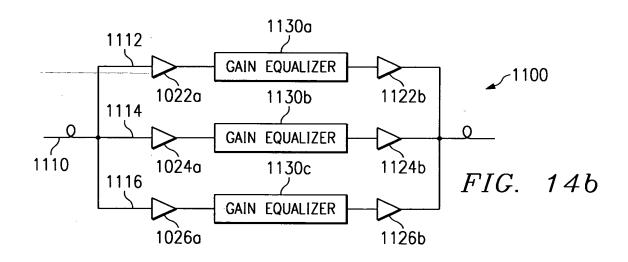
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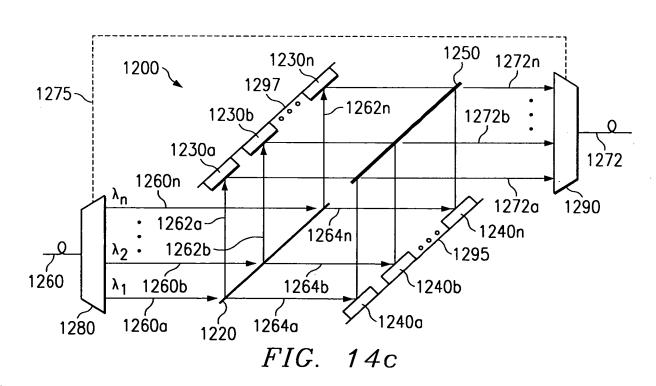


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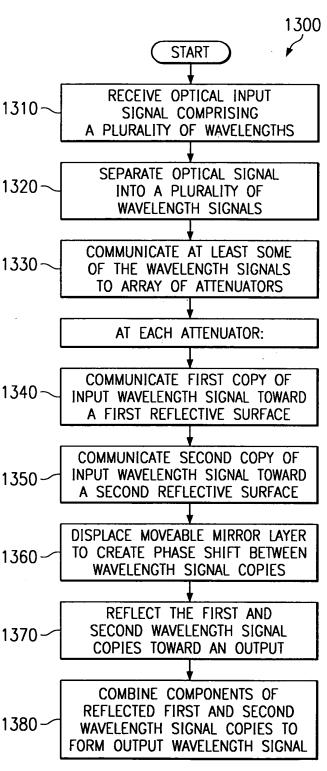
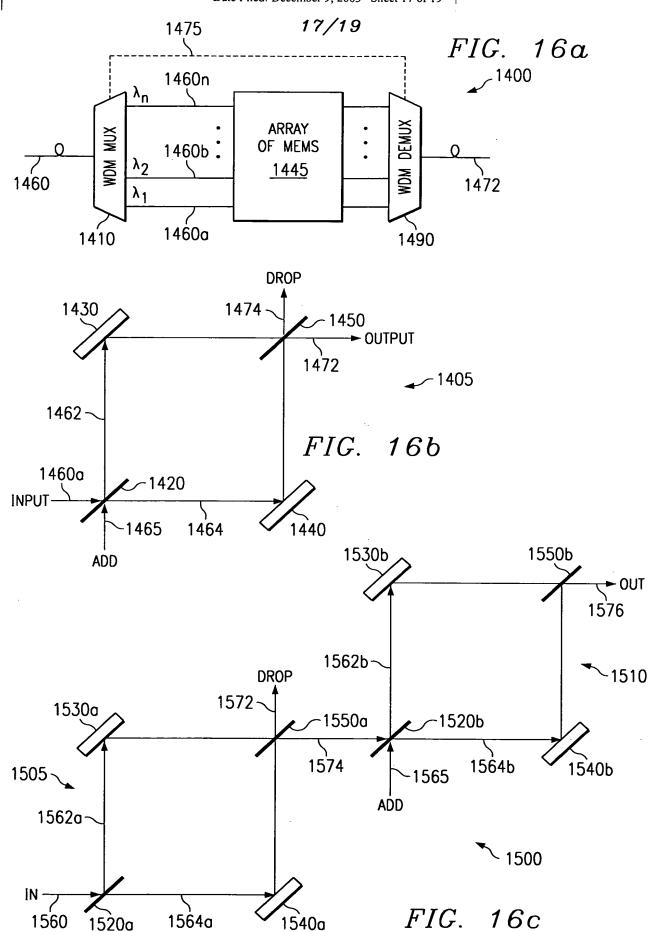


FIG. 15

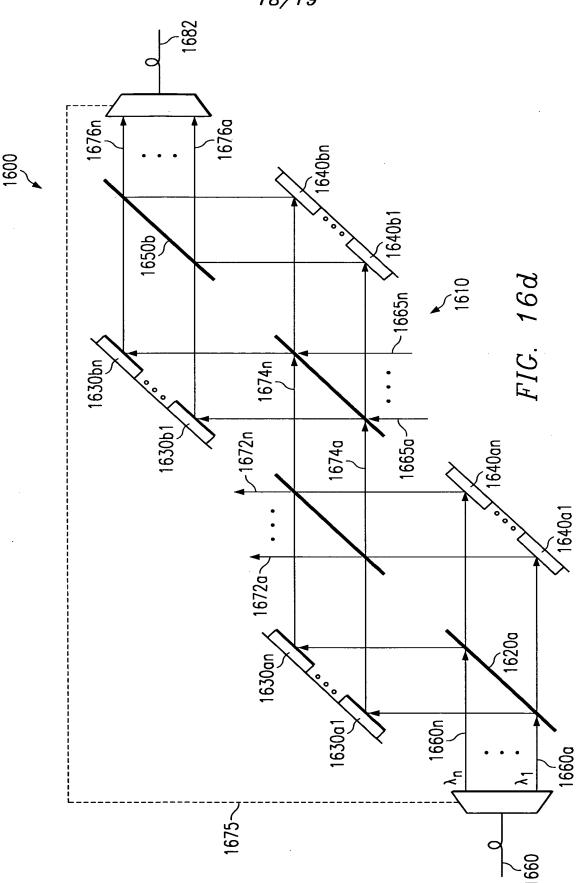
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